SBC4661 Ordering Configuration

SBC4661 – B V L

FPGA SIZE
A: 70T
B: 160T (standard)
C: 325T
D: 410T
E: No FPGA

CAMERA INTERFACE
M: MIPI CSI
N: NTSC/PAL
F: FLIR® Thermal Imaging
V: GigE & USB3 Vision (standard)
X: CoaXPress
L: CameraLink

OPERATING SYSTEM
A: Android
L: Linux (standard)
W: WindowsCE

UPGRADE OPTIONS	
4661OPT8	Upgrade to 8GB flash
4661OPT24	CFAST Interface
4661OPT45	Audio Interface
4661OPT48	First Video Decoder

MICRO/SYS

DK4661 Ordering Configuration

DK 4661 – BVL

MICRO/SYS SBC DEVELOPMENT KIT
DK: Standard Development Kit
DKF: FPGA Development Kit
DKV: Vision Development Kit
DKC: FPGA & Vision Development Kit

SBC4661 CONFIGURATION
Configure the SBC4661 in your Kit
(see SBC4661 Ordering Configurations)

TYPE	CONTENTS
DK	SBC4661, Complete Cable Set, Linux Yocto BSP
DKF	DK **+** Xilinx FPGA Platform Cable, Yocto FPGA Layer
DKV	DK **+** Camera Module, Yocto Vision Layer
DKC	DK **+** DKF **+** DKV